

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

) Confirmation No.: 1914
)
) Group Art Unit: 2826
)
Applicants: WATTS et al.) Examiner: Andujar, Leonardo
)
Application No.: 10/729,544) RESPONSE TRANSMITTAL
)
Filing Date: December 5, 2003) Docket No.: P17173
)
For: STACKED INTEGRATED CIRCUIT) PTO Customer Number 28062
PACKAGES AND METHODS OF) Buckley, Maschoff & Talwalkar LLC
MAKING THE PACKAGES) Attorneys for Intel Corporation
) 50 Locust Avenue
) New Canaan, CT 06840
)

Mail Stop Appeal Brief - Patents
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith for filing are:

1. ☒ Response to Examiners Answer mailed March 21, 2007.
2. ☒ While no fees are believed due in conjunction with this response, the Commissioner is hereby authorized to charge and credit Deposit Account No. 50-1852 as described below. A duplicate copy of this sheet is enclosed.
 - i. ☒ Credit any overpayment.
 - ii. ☒ Charge any additional fees required under 37 CFR 1.16 and 1.17

Respectfully submitted,

May 17, 2007
Date

/NL/
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